



10/15

TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application No.	10/689,291	
	Filing Date	October 20, 2003	
	First Named Inventor	Patrice Roussel	
	Art Unit	2183	
	Examiner Name	Chan, Eddie P.	
Total Number of Pages in This Submission	10	Attorney Docket Number	42P4920C4

ENCLOSURES (check all that apply)		
<input checked="" type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment / Response <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input checked="" type="checkbox"/> PTO/SB/08 <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Basic Filing Fee <input type="checkbox"/> Declaration/POA <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s)	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): <div style="border: 1px solid black; padding: 5px; margin-top: 5px;"><ul style="list-style-type: none">- First Class Certificate of Mailing;- Stamped Return postcard- Copies of twenty-three (23) of the cited references</div>
Remarks		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Brent E. Vecchia, Reg. No. 48,011 BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
Signature	<i>Brent E. Vecchia</i>
Date	12-20-04

CERTIFICATE OF MAILING/TRANSMISSION			
I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.			
Typed or printed name	Krista Mathieson		
Signature	<i>Krista Mathieson</i>	Date	December 20, 2004



FEE TRANSMITTAL for FY 2005

Patent fees are subject to annual revision.

Complete if Known

Application Number 10/689,291
Filing Date October 20, 2003
First Named Inventor Patrice Roussel
Examiner Name Chan, Eddie P.
Art Unit 2183
Attorney Docket No. 42P4920C4

☐ Applicant claims small entity status. See 37 CFR 1.27.

TOTAL AMOUNT OF PAYMENT (\$) 0.00

METHOD OF PAYMENT (check all that apply)

☐ Check ☐ Credit card ☐ Money Order ☐ None ☐ Other (please identify):

☒ Deposit Account Deposit Account Number: 02-2666 Deposit Account Name: Blakely, Sokoloff, Taylor & Zafman LLP

For the above-identified deposit account, the Director is hereby authorized to: (check all that apply)

☐ Charge fee(s) indicated below ☐ Charge fee(s) indicated below, except for the filing fee
☒ Charge any additional fee(s) or underpayment of fee(s) under 37 CFR §§ 1.16, 1.17, 1.18 and 1.20. ☒ Credit any overpayments

FEE CALCULATION

1. EXTRA CLAIM FEES

Total Claims	Extra Claims	Fee from below	Fee Paid
47	47	0	\$0.00
14	14	0	\$0.00
Multiple Dependent			
Large Entity	Small Entity		
Fee Code	Fee Code	Fee Description	
1202 50	2202 25	Claims in excess of 20	
1201 200	2201 100	Independent claims in excess of 3	
1203 360	2203 180	Multiple Dependent claim, if not paid	
1204 300	2204 150	**Reissue independent claims over original patent	
1205 300	2205 150	**Reissue claims in excess of 20 and over original patent	
SUBTOTAL (1)		(\$)	0.00

**or number previously paid, if greater, For Reissues, see below

2. ADDITIONAL FEES

Large Entity	Small Entity	Fee Description	Fee Paid
Fee Code	Fee Code		
1051 130	2051 65	Surcharge - late filing fee or oath	
1052 50	2052 25	Surcharge - late provisional filing fee or cover sheet	
2053 130	2053 130	Non-English specification	
1251 120	2251 60	Extension for reply within first month	
1252 450	2252 225	Extension for reply within second month	
1253 1,020	2253 510	Extension for reply within third month	
1254 1,590	2254 795	Extension for reply within fourth month	
1255 2,160	2255 1,080	Extension for reply within fifth month	
1401 500	2401 250	Notice of Appeal	
1402 500	2402 250	Filing a brief in support of an appeal	
1403 1,000	2403 500	Request for oral hearing	
1451 1,510	2451 1,510	Petition to institute a public use proceeding	
1460 130	2460 130	Petitions to the Commissioner	
1807 50	1807 50	Processing fee under 37 CFR 1.17(q)	
1806 180	1806 180	Submission of Information Disclosure Stmt	
1809 790	1809 395	Filing a submission after final rejection (37 CFR § 1.129(a))	
1810 790	2810 395	For each additional invention to be examined (37 CFR § 1.129(b))	
Other fee (specify)			
SUBTOTAL (2)		(\$)	

SUBMITTED BY

Name (Print/Type) Brent E. Vecchia Registration No. 48,011 Telephone (303) 740-1980
Signature Brent E. Vecchia Date 12-20-04



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

PATRICE ROUSSEL, ET AL.

Application No.: 10/689,291

Filed: October 20, 2003

For: **Staggering Execution of a Single Packed
Data Instruction Using the Same Circuit**

Art Group: 2183

Examiner: Chan, Eddie P.

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a first Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

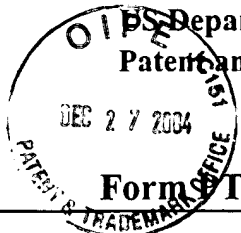
Date: 12-20-04

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Krista Mathieson 12/20/04
Krista Mathieson *Date*

 <p>Department of Commerce Patent and Trademark Office</p> <p>Form TO-1449 (Modified)</p>		Atty. Docket No.: 042390.P4920C4		Application No.: 10/689,291			
		Applicant: Roussel et al.					
		Filing Date: October 20, 2003					
US Patent Documents							
Examiner's Initials		Date	Document Number	Name	Class	Sub- Class	Filing Date
		06/30/87	4,677,582	Nagafuji			
		07/26/88	4,760,545	Inagami et al.			
		03/07/89	4,811,269	Hirose et al.			
		07/31/90	4,945,479	Rusterholz et al.			
		11/20/90	4,972,362	Elkind et al.			
		01/15/91	4,985,848	Pfeiffer et al.			
		03/19/91	5,001,662	Baum			
		09/10/91	5,047,975	Patti et al.			
		01/14/92	5,081,698	Kohn			
		06/30/92	5,126,964	Zurawski			
		11/30/92	5,161,247	Murakami et al.			
		02/23/93	5,189,636	Patti et al.			
		08/31/93	5,241,493	Chu et al.			
		07/05/94	5,327,369	Ashkenazi			
		02/14/95	5,390,135	Lee et al.			
		05/07/96	5,515,520	Hatta et al.			
		11/26/96	5,579,253	Lee et al.			
		12/31/96	5,590,365	Ide et al.			
		12/23/97	5,701,508	Glew et al.			
		04/21/98	5,742,840	Hansen et al.			
		07/07/98	5,778,419	Hansen et al.			
Foreign Patent Documents							
Examiner's Initials		Date	Document Number	Country	Class	Sub- Class	Translation
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)							
Examiner				Date Considered			

Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw a line through the citation if not in conformance and not considered. Include a copy of this form with the next communication to the applicant

US Department of Commerce Patent and Trademark Office Form PTO-1449 (Modified)			Atty. Docket No.: 42390P4920C4		Application No.: 10/689,291		
			Applicant: Roussel et al.				
			Filing Date: October 20, 2003				
US Patent Documents							
Examiner's Initials		Date	Document Number	Name	Class	Sub- Class	Filing Date
		08/11/98	5,794,060	Hansen et al.			
		08/11/98	5,794,061	Hansen et al.			
		09/01/98	5,802,336	Peleg et al.			
		09/08/98	5,806,049	Petruzzi			
		09/15/98	5,809,321	Hansen et al.			
		10/06/98	5,819,101	Peleg et al.			
		10/13/98	5,822,603	Hansen et al.			
		11/10/98	5,835,392	Dulong et al.			
		03/16/99	5,883,824	Lee et al.			
		03/16/99	5,884,071	Kosaraju			
		09/14/99	5,953,241	Hansen et al.			
		11/09/99	5,983,257	Dulong et al.			
		12/21/99	6,006,318	Hansen et al.			
		03/07/00	6,035,387	Hsu et al.			
		07/10/01	6,260,137	Fleck et al.			
		09/25/01	6,295,599	Hansen et al.			
		04/23/02	6,378,060	Hansen et al.			
		05/07/02	6,385,634	Peleg et al.			
		10/22/02	6,470,370	Fischer et al.			
		06/24/03	6,584,482	Hansen et al.			
		11/04/03	6,643,765	Hansen et al.			
		04/20/04	6,725,356	Hansen et al.			
Foreign Patent Documents							
Examiner's Initials		Date	Document Number	Country	Class	Sub- Class	Translation
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)							
Examiner				Date Considered			

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Examiner's Initials		Date	Document Number	Name	Class	Sub- Class	Filing Date
Foreign Patent Documents							
Examiner's Initials		Date	Document Number	Country	Class	Sub- Class	Translation
		12/01/00	GB 2 339 040 A	Great Britian			
		10/14/99	DE 199 14 617A1	Germany			
		10/07/99	WO 99/50740	PCT			
		04/23/87	DE 36 36 106A1	Germany			
		07/03/97	WO 97/23821	PCT			
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)							
		Santoro, Mark; Design and Clocking of VLSI Multipliers. Technical Report No. CSL -TR-89-397, October 1989. Pages i-xii and 1-118.					
		Santoro, Mark, et al.; SPIM: A Pipelined 64X64 bit Iterative Multiplier, IEEE Journal of Solid -State Circuits, Vol. 24, No. 2, April 1989. Pages 487-493.					
		Santoro, Mark, et al.; SESSION II: HIGH-SPEED MICROPROCESSOR. WAM 2.6: A Piplelined 64X64b Iterative Array Multiplier. 1988 IEEE International Solid State Circuits Conference . Pages 36-37 and 290.					
		BIT Preliminary, Bipolar Integrated Technology, Inc. B3110/B3120;B2110/B2120 Floating Point Chip Set. Pages 1-40.					
		Eklind, Bob, et al. A SUB 10 nS Bipolar 64 Bit Integrated/Floating Point Processor Implemented On Two Circuits. IEEE 1987 BCTM, pages 101- 104					
		Leibowitz, Bob; et al., System Design; ECL gains ground in battle against CMOS. Computer Design; April 1, 1987. Pages 91-95.					
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Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)							
		IBM. TBD: Double Speed, Single Precision Vector Register Organization Using Double Port Chips. Feb. 1981. Pp.1-6.					
		Farmwald, P. Michael; High Bandwidth Evaluation of Elementary Functions, S-1 Project. IEEE 1981. Pages 139 - 142.					
		Farmwald, P. Michael; On the Design of High Performance Digital Arithmetic Units. UCRL-53190. August 1981. Pages i-vii and 1-95.					
		Grimes et al.; 64-Bit Processor. The Intel i860 64-Bit Processor: A General-Purpose CPU with 3D Graphics Capabilities. July 1989. Pages 85-94.					
		Ide, et al.; A 320-MFLOPS CMOS Floating-Point Processing Unit for Superscalar Processors. IEEE Journal of Solid State Circuits, Vol. 28, No. 3, March 1993. Pages 352 - 361.					
		Lino, et al.; ISSCC 92 SESSION 6/ MICROPROCESSORS/ TA 6.5: A 289MFLOPS Single-Chip Supercomputer. 1992 IEEE International Solid State Circuits Conference. Pages 112-113.					
		Kohn, et al.; Introducing the Intel i860 64 Bit Microprocessor, Intel Corp. August 1989. Pages 15 - 30.					
		Lee, Ruby B.; Accelerating Multimedia with Enhanced Microprocessors. Hewlett-Packard. IEEE Micro, April 1995. Pages 22-32.					
		Lee, Ruby B.; Realtime MPEG Video via Software Decompression on a PA-RISC Processor. 1995 IEEE, pages 186-192.					
Examiner				Date Considered			

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Foreign Patent Documents							
Examiner's Initials		Date	Document Number	Country	Class	Sub- Class	Translation
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)							
		Manferdelli, John L. et al.; Signal Processing Aspects of the S-1 Multiprocessor Project. UCRL-84658. July 28, 1980. Pages 1-8.					
		Spaderna, D., et al.; An Integrated Floating Point Vector Processor for DSP and Scientific Computing". SHARP Microelectronics Technology Inc. IEEE 1989. Pages 8-13.					
		Undy, Steve, et al.; A Low-Cost Graphics and Multimedia Workstation Chip Set. April 1994, IEEE micro, pages 10 - 22.					
Examiner			Date Considered				

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